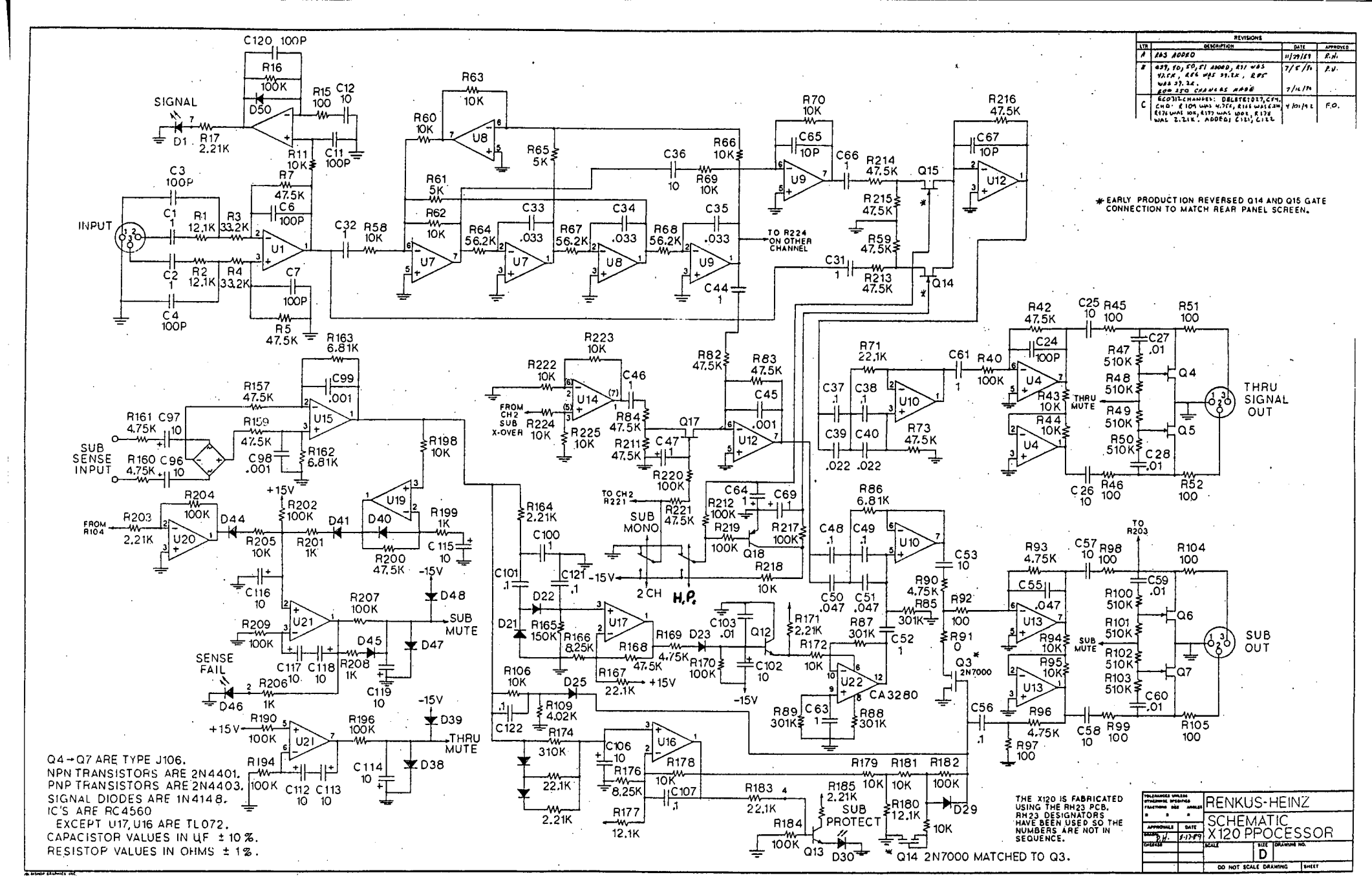

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	R85 ADDED	11/29/89	R.H.
B	439, 50, 50, 51 ADDED, R31 WAS 93.1K, R26 WAS 39.2K, R25 WAS 37.3K. ADD 120 CHANGES MADE	7/5/90	R.H.
C	ECO 312-CHANGES: DELETE 1037, C99, CHG: R 10M WAS 4.75K, R165 WAS 63K, R176 WAS 10K, R177 WAS 100K, R178 WAS 2.21K. ADDED C121, C122	9/20/90	F.O.

* EARLY PRODUCTION REVERSED Q14 AND Q15 GATE CONNECTION TO MATCH REAR PANEL SCREEN.

Q4→Q7 ARE TYPE J106.
NPN TRANSISTORS ARE 2N4401.
PNP TRANSISTORS ARE 2N4403.
SIGNAL DIODES ARE 1N4148.
IC'S ARE RC4560
EXCEPT U17, U16 ARE TL072.
CAPACISTOR VALUES IN µF ± 10%.
RESISTOP VALUES IN OHMS ± 1%.

THE X120 IS FABRICATED USING THE RH23 PCB. RH23 DESIGNATORS HAVE BEEN USED SO THE NUMBERS ARE NOT IN SEQUENCE.

* Q14 2N7000 MATCHED TO Q3.

TOLERANCES UNLESS OTHERWISE SPECIFIED: FRACTIONS ±1/64, ANGLES ±1°		RENKUS-HEINZ
APPROVALS	DATE	SCHEMATIC X120 PPOCESSOR
DRAWN R.H.	8-17-89	
CHECKED		SCALE — SIZE D — DRAWING NO.
		DO NOT SCALE DRAWING — SHEET

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