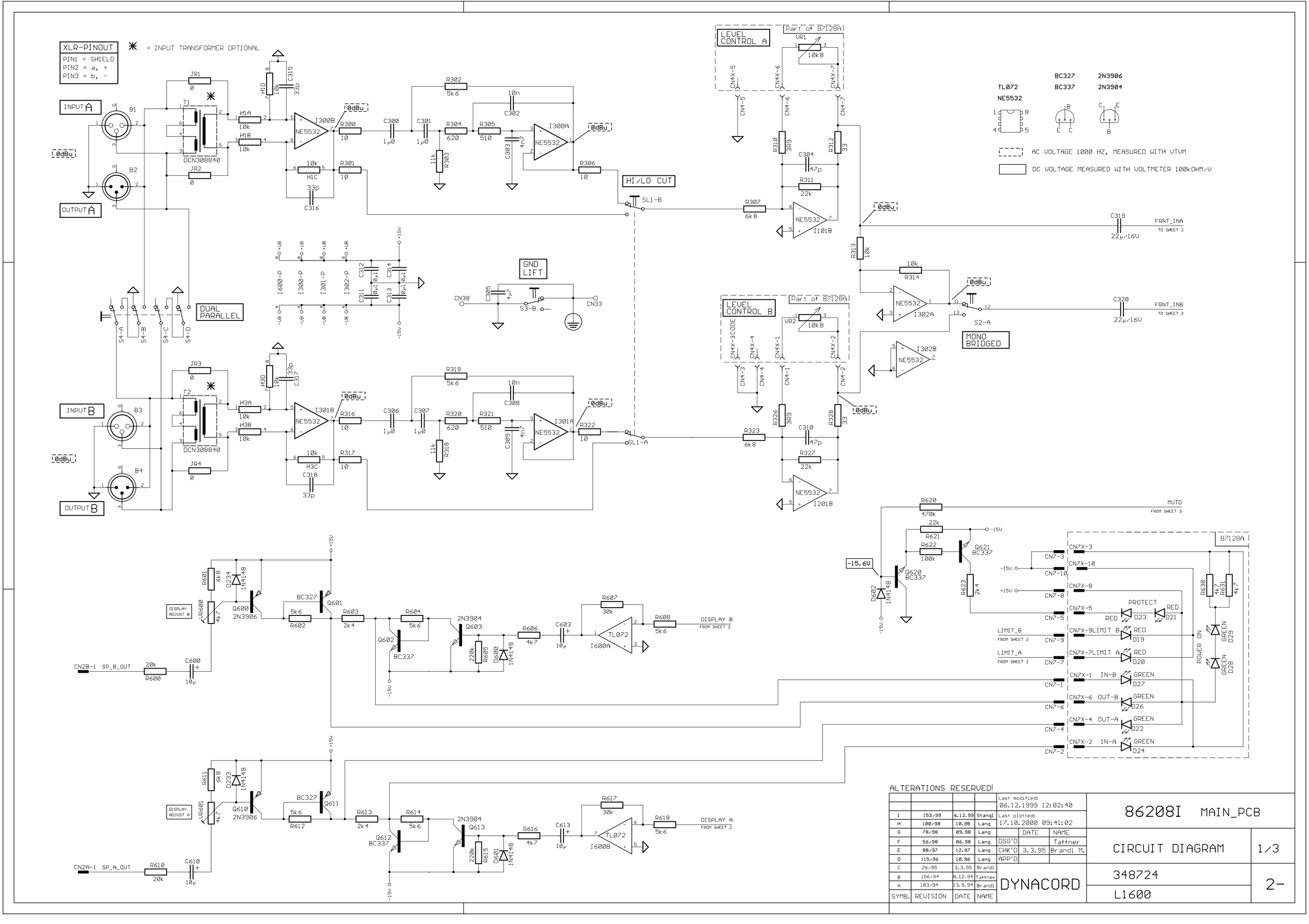
XLR-PINOUT
PIN1 = SHIELD
PIN2 = a, +
PIN3 = b, -
* = INPUT TRANSFORMER OPTIONAL
INPUT A
OUTPUT A
INPUT B
OUTPUT B
DUAL PARALLEL
GND LIFT
HI/LO CUT
LEVEL CONTROL A
LEVEL CONTROL B
Part of 87128A
MONO BRIDGED
TL072
NE5532
BC327
BC337
2N3906
2N3904
AC VOLTAGE 1000 HZ, MEASURED WITH VTVM
DC VOLTAGE MEASURED WITH VOLTMETER 100kOHM/V
FRNT_INA
FRNT_INB
MUTD
DISPLAY B
DISPLAY A
DISPLAY ADJUST B
DISPLAY ADJUST A
SP_B_OUT
SP_A_OUT
LIMIT_B
LIMIT_A
PROTECT
POWER ON
87128A
ALTERATIONS RESERVED!
Last modified: 06.12.1999 12:02:40
Last plotted: 17.10.2000 09:41:02
86208I MAIN_PCB
CIRCUIT DIAGRAM
1/3
348724
L1600
2-
DYNACORD
DSG'D Taffner
CHK'D 3.3.95 Brandl M.
APP'D
SYMB. REVISION DATE NAME
I 153/99 6.12.99 Stangl
H 100/98 10.98 Lang
G 78/98 09.98 Lang
F 56/98 06.98 Lang
E 88/97 12.97 Lang
D 115/96 10.96 Lang
C 26/95 3.3.95 Brandl
B 156/94 8.12.94 Taffner
A 103/94 13.9.94 Brandl