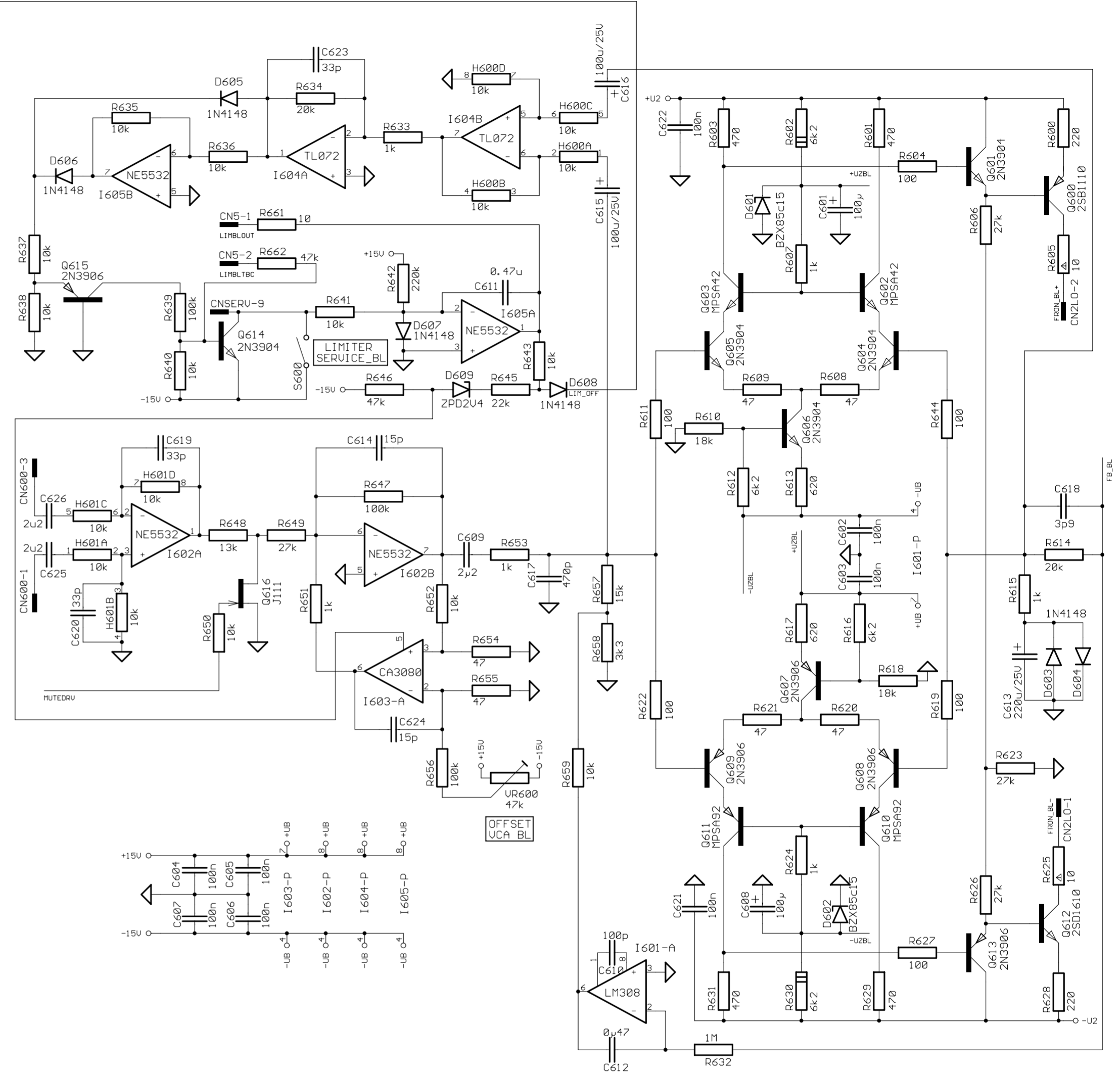
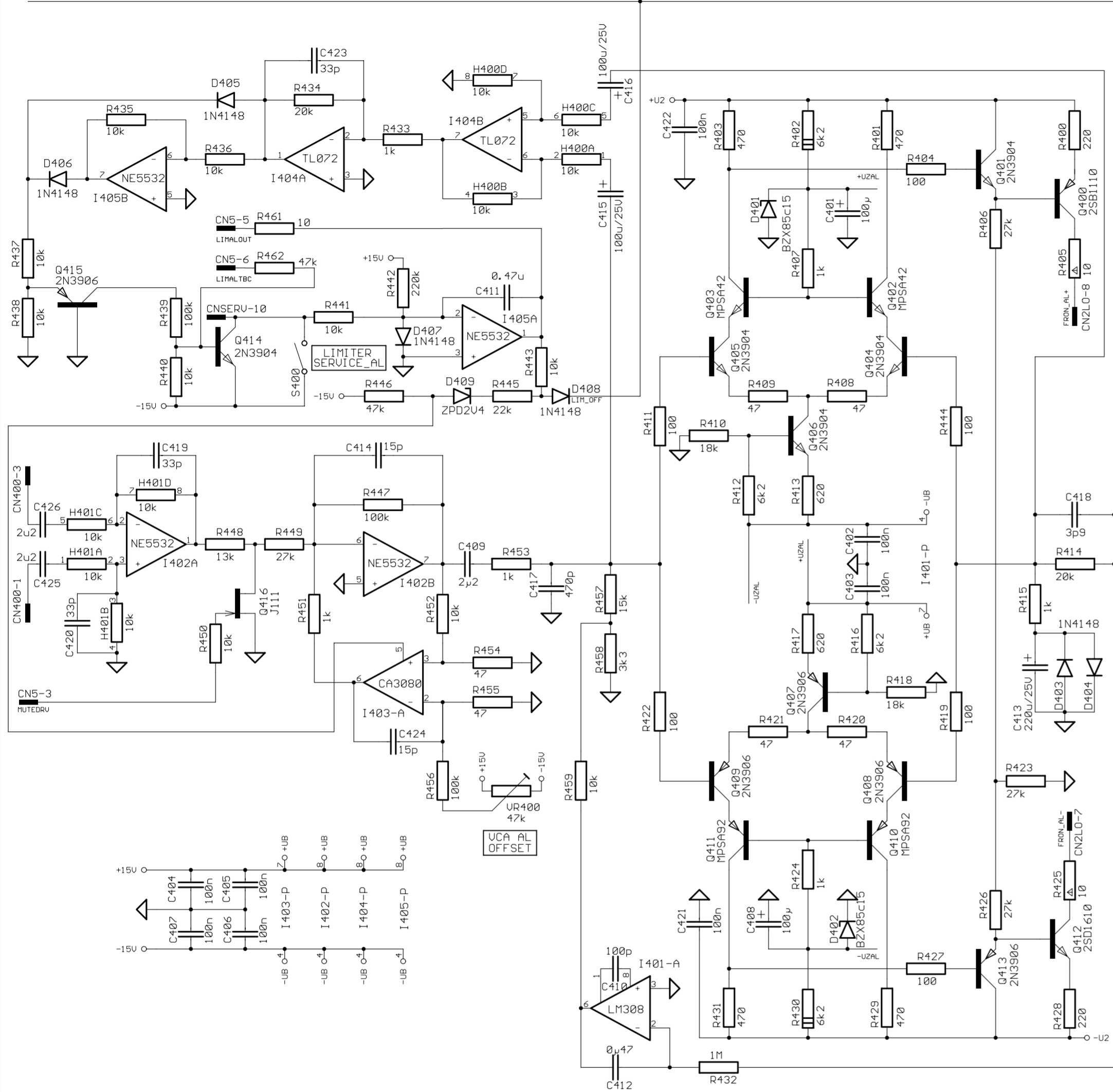


FRONTEND A_LOW

FRONTEND B_LOW

LIM_OF



CNSERV	
1	-15V
2	+15V
3	Fan-Voltage
4	GND
5	Lim_Off
6	GND
7	Lim_Serv_v_BH
8	Lim_Serv_v_AH
9	Lim_Serv_v_BL
10	Lim_Serv_v_AL
11	+U1
12	-U1
13	+U2
14	-U2

CN2LO	
1	Input- BL
2	Input+ BL
3	NTC BL
4	+15V
5	NTC AL
6	SOA-Sense
7	Input- AL
8	Input+ AL

CN5	
1	LIMBLOUT
2	LIMBLTBC
3	Mutedrive
4	n.c.
5	LIMALOUT
6	LIMALTBC

ALTERATIONS RESERVED!

D	58/99	05.99	CSC
C	13/97	05.03.	Lang
B	60/96	08.05.	Brand
A	Nullserie	16.04.	Brand
SYMB.	REVISION	DATE	NAME

DYNACORD

86227C MAIN_PCB

CIRCUIT DIAGRAM

SP3/5/7 / XA2600 / PM2600

351759

/4

2-

The diagrams show the following components and their pin configurations:

- 2N3904**: A TO-18 package with three pins labeled E (Emitter), C (Collector), and B (Base).
- MPSA92**: A TO-18 package with three pins labeled C (Collector), E (Emitter), and B (Base).
- 2N3906**: A TO-18 package with three pins labeled C (Collector), E (Emitter), and B (Base).
- MPSA92**: A TO-18 package with three pins labeled C (Collector), E (Emitter), and B (Base).
- J111**: A TO-18 package with three pins labeled G (Gate), S (Source), and D (Drain).
- LM308**: An 8-pin DIP package with pins numbered 1 through 8.
- TL072**: An 8-pin DIP package with pins numbered 1 through 8.
- CA3080**: An 8-pin DIP package with pins numbered 1 through 8.
- NE5532**: An 8-pin DIP package with pins numbered 1 through 8.

SAFETY COMPONENT
(MUST BE REPLACED BY ORIGINAL PART)

AC VOLTAGE 1000 HZ, MEASURED WITH VTVM

DC VOLTAGE MEASURED WITH VOLTMETER 100kOHM/V

CNSERV	
1	-15V
2	+15V
3	Fan-Voltage
4	GND
5	Lim_Off
6	GND
7	Lim_Serv_v_BH
8	Lim_Serv_v_AH
9	Lim_Serv_v_BL
10	Lim_Serv_v_AL
11	+U1
12	-U1
13	+U2
14	-U2

CN2LO	
1	Input- BL
2	Input+ BL
3	NTC BL
4	+15V
5	NTC AL
6	SOA-Sense
7	Input- AL
8	Input+ AL

CN5	
1	LIMBLOUT
2	LIMBLTBC
3	Mutedrive
4	n.c.
5	LIMALOUT
6	LIMALTBC

ALTERATIONS RESERVED!

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DYNACORD

86227C MAIN_PCB

CIRCUIT DIAGRAM

SP3/5/7 / XA2600 / PM2600

351759

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