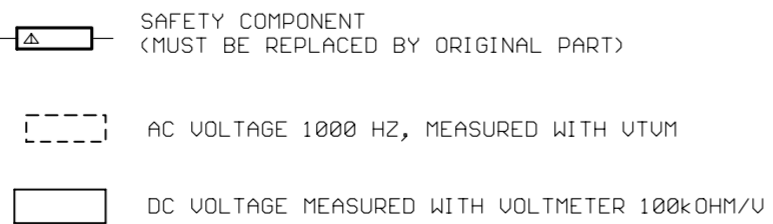
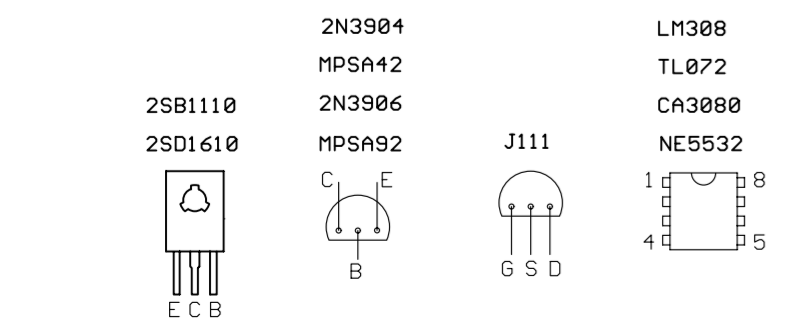
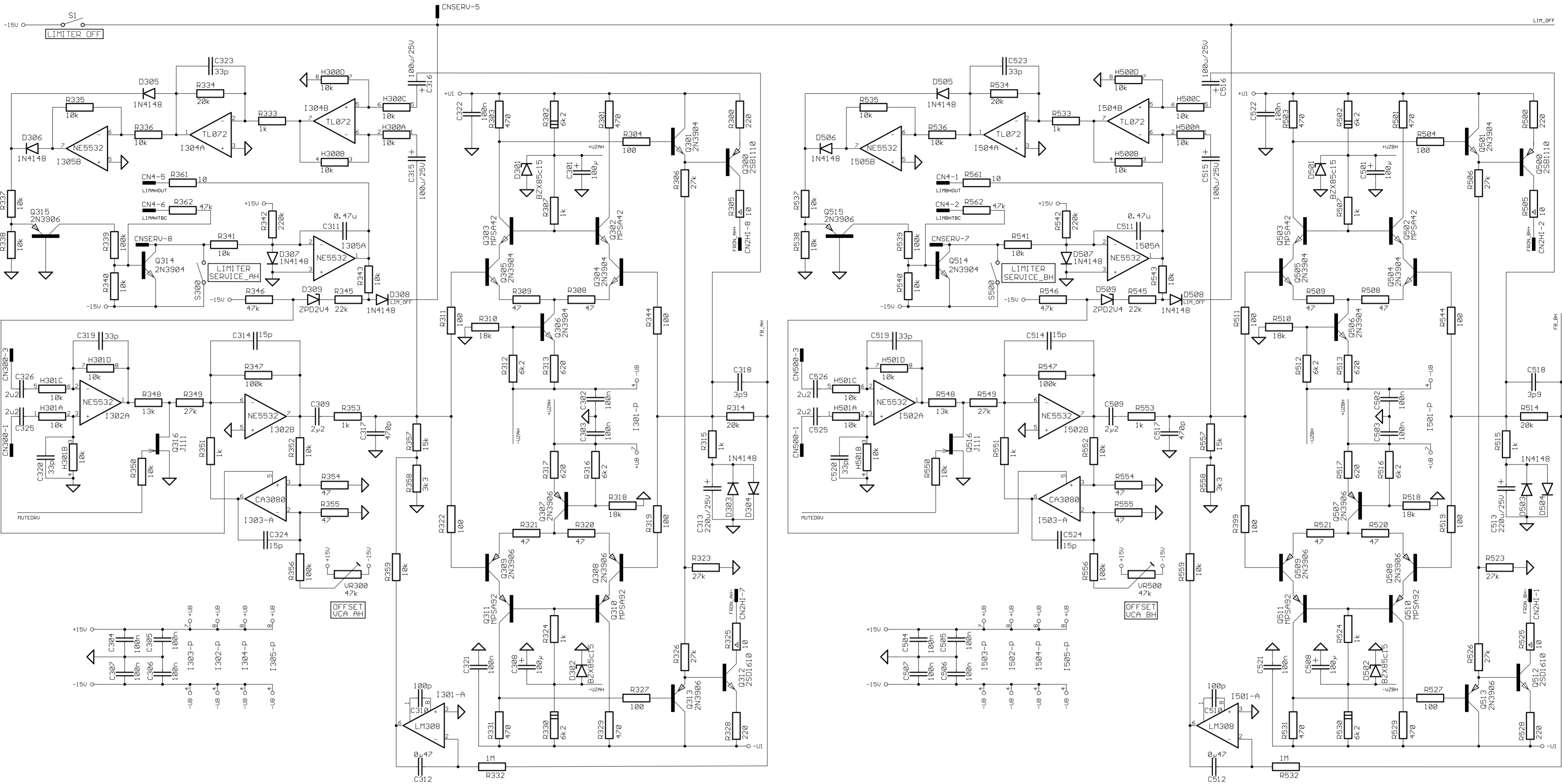


FRONTEND A_HIGH

FRONTEND B_HIGH



CNSERV	
1	-15V
2	+15V
3	Fan-Voltage
4	GND
5	Lim_Off
6	GND
7	Lim_Serv_BH
8	Lim_Serv_AH
9	Lim_Serv_BL
10	Lim_Serv_AL
11	+U1
12	-U1
13	+U2
14	-U2

CN2HI	
1	Input- BH
2	Input+ BH
3	NTC BH
4	+15V
5	NTC AH
6	SOA-Sense
7	Input- AH
8	Input+ AH

CN4	
1	LIMBHOUT
2	LIMBHTBC
3	n.c.
4	n.c.
5	LIMAHOUT
6	LIMAHHTBC

ALTERATIONS RESERVED!

					Last modified 20.05.1999 10:57:02
					Last plotted 23.04.2004 12:04:08
					1996 DATE NAME
					DSG'D 21.2. M. Brandl
					CHK'D 21.2. M. Brandl
					APP'D
D	58/99	05.99	CSC		
C	13/97	05.03.	Lang		
B	60/96	08.05.	Brandl		
A	Nullserie	16.04.	Brandl		
SYMB.	REVISION	DATE	NAME		

DYNACORD

86227C MAIN_PCB	
CIRCUIT DIAGRAM	3/4
SP3/5/7 / XA2600 / PM2600	
351759	
2-	